

<b>Hardware Software Partitioning of Control Data Flow Graph on System On Programmable Chip (type Research)</b>	
<b>Date : 03/02/2015----03/06/2014</b>	2015/10/01 - 2018/09/30
<b>Encadrant(s) : Bouraoui oui</b>	
<b>Description:</b> CIC-560 training system is designed based on the latest digital technology in conjunction with EDA experimental teaching material. This training system consists of FPGA chip with higher logic elements and large number of pins. Therefore, the aim is help students to develop, implement and verify design of basic or advanced digital circuit	[1] <a href="http://www.kandh.com.tw/es/products_2.php?prod=45">http://www.kandh.com.tw/es/products_2.php?prod=45</a>
<b>Mots clés :</b> developing, implementation and verifying design of basic or advanced digital circuit	
<b>Département(s) :</b> EI	
<b>Financement :</b> ---	