Algorithms for multi-component digital systems to minimize dynamic power	
Date : 03/02/2015----03/06/2014	2015/10/01 - 2018/09/30
Encadrant(s) : Bouraoui ouni	
Description: The aim is to develop new scheduling algorithms that provide a decrease in dynamic power consumption through achieving components scheduling. The major idea of those algorithms is to amplify the latency of some components whenever it is feasible without negatively influencing the dependency constraints. For that purpose, we need to decrease the frequency of the components clocks. Since frequency and power are inter-related, a decrease in frequency value will be followed by a decrease in power consumption.	[1] B. Suresh Babu, A. Shunmugalatha, "Reducing Power Losses in Power System by Using Self Adaptive Firefly Algorithm", Swarm, Evolutionary, and Memetic Computing Lecture Notes in Computer Science Volume 8297, 2013, pp 122-132 [2] Lizhe Wang, von Laszewski, G. Dayal, J.,Fugang Wang, "Towards Energy Aware Scheduling for Precedence Constrained Parallel Tasks in a Cluster with DVFS", IEEE/ACM International Conference Cluster, Cloud and Grid Computing (CCGrid), 2010
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